

MICROSTRIP DC-BLOCK ON TEXTILES USING ONLY SELF-ADHESIVE COPPER TAPE

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Abstract: Self-adhesive copper tape with non-conductive glue is used for DC-block on textiles. As capacitance, overlapping of copper strips was used, and the dielectric is the non-conductive glue of the strips. Two overlapping lengths were realized and measured. Due to the specificity of the textile, especially when worn, the cases of flat structure and when it is in S-shape were measured. The structure is easy to put on and take off and does not damage the textile. By easily correcting the length of the overlap, the range of impervious lower frequencies can be changed.

Keywords: Microwave on textiles, DC-Block, Self-Adhesive Copper Tape

1. INTRODUCTION

The application of microwave structures on textiles is becoming more and more common [1-4]. In this case, the technique must be adapted to both the textile substrate and the applicable metallization.

Textile as a substrate is soft, flexible, somewhat anisotropic and rough. There is no precisely determined dielectric constant and no precisely determined thickness [3,4].

Conductive thread (embroidery), direct application of a conductive layer (paste, ink, etc.) and conductive self-adhesive tapes are the basic techniques of metallization on textiles. Conductive self-adhesive tapes have several advantages [4-6]. The most important are [4]: easy to put on and take off without damaging the textile, metallization similar to printed circuit boards (well-defined conductive layer) and easy formation of multi-layered (overlapped) structures.

DC-blockers in microstrip are important structures in microwaves. They are predominantly constructed on the basis of lateral coupling [7-9], which occupies more surface area and requires a narrow lateral gap for capacity. The solution can be also vertical coupling [10], which includes a multi-layered structure but without a narrow lateral gap.

The vertical coupling is more suitable for metallization on textiles. It is easier and more precise processing with easier formation of a multi-layered structure with avoided lateral coupling. DC-block on textile substrate using

vertical coupling is given in [11]. The capacity is formed by screen printing of silver on commercially-available polyurethane (PU) film 50 μm thick as the interface layer on textile (felt). An additional thermal treatment was done to fix the PU dielectric on the textile which complicates the process.

A simpler structure can be formed just using direct overlap of two Cu tapes with non-conductive glue (around 15 μm thick and around $\epsilon_r = 1.3$) as a dielectric layer [12]. This makes direct use of the possibility of easy formation of overlapped layers using Cu tapes. Substrate is felt about $h=0.9$ mm, dielectric constant around $\epsilon_r = 1.3$.

2. REALIZATION AND MEASUREMENT

An example of the structure is given in Fig. 1 [12] where the overlap area on the microstrip line represents the DC-block. The overlap area is between the black vertical lines in Fig.1. Overlapping was first realized on adequately cut 4 mm wide strips for 50 Ω microstrip. One strip is glued to the appropriate length over the other and then they are glued together to the textile. A special self-adhesive tape was used to transfer the formed structure [4]. Simulations for low frequencies depending on the capacitance between two microstrip structures are given in Fig. 2.

The structures were measured flat and S-shaped, Fig. 3. The S-shape was chosen as the most complicated shape in clothing textiles. Two overlaps are fabricated: 11 mm and 5 mm. Both are measured flat and S-shaped.

First, a structure with an overlap of 11 mm was made and measured, Fig.4. The measured attenuation value of 1 dB

at 0.1 GHz corresponds to a capacitance of about 30 pF in low frequency simulation. The results are good up to about 5 GHz for the flat structure, while for the S-shape it is about 4.5 GHz, and that is because of the S_{11} .

The overlap part was then trimmed to 5 mm and the new structure was also measured, Fig.5. The measured attenuation value of 3 dB at 0.1 GHz corresponds to a capacitance of about 15 pF in low frequency simulation. The results are good up to near 5.5 GHz for the both flat and the S-shape structure.



Figure 1. DC-block construction using Cu adhesive tape on felt substrate [12]. The overlap area on the microstrip line is between the black vertical lines.

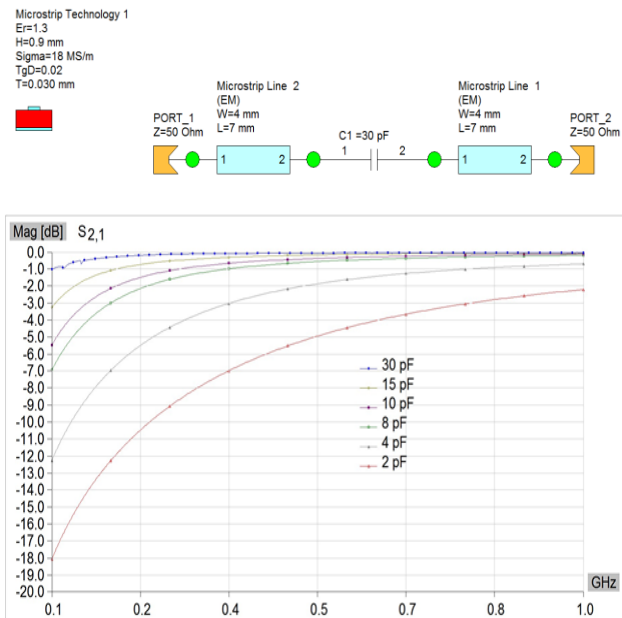


Figure 2. Simulations for low frequencies depending on capacity: simple model and results. Lower S_{21} corresponds to lower capacity [12]



Figure 3. S-shape structure

3. CONCLUSION

The paper presents microstrip DC-block on textile with use only self-adhesive copper tape. Implementation is

simple; easy to put on and easy to take off, which supports the sustainability of textiles. The dielectric is just a non-conductive glue between the overlap of two copper strips.

The folds are realized as 11 mm and 5 mm, with the second realized as trimming of the first. The S-shape measurement in addition to the flat state measurement perturbed the S-parameters very little. Logically, an 11 mm overlap has a lower DC-block than a 5 mm overlap. The latter has a slightly higher acceptable higher frequency. Good for lower microwave frequencies in communications, up to 6 GHz.

This type of DC-block structure is easy to apply and occupies a small area due to vertical coupling. This especially applies to textile structures, especially clothing. As it is easy to put on, it is also easy to take off without damaging the textile. It is suitable for suppressing low frequencies and DC in military application without complicated technological process such as printing and embroidery.

Disadvantage can be the imperfection of textiles as a substrate for microwaves. Also, it depends on the fineness of the workmanship.

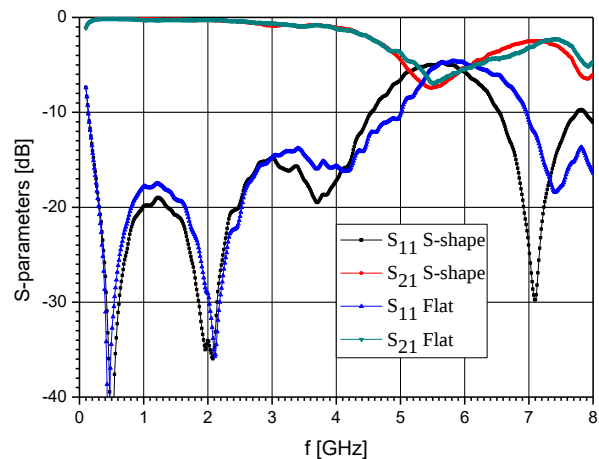


Figure 4. Overlap 11 mm: The measured S-parameters, flat and S-shape, 0.1 GHz to 8 GHz

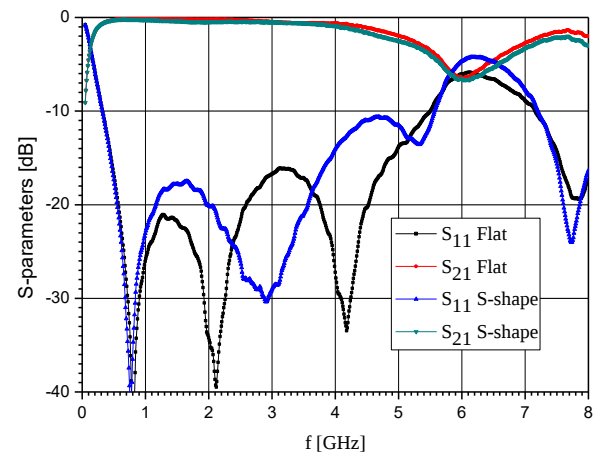


Figure 5. Overlap 5 mm: The measured S-parameters, flat and S-shape, 0.1 GHz to 8 GHz

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